



# NES-MFZUC+

## Zynq UltraScale + FPGA Module

### ▶ HIGHLIGHTS

Xilinx Zynq UltraScale+ ZU3EG/4EV/5EV MPSoC based on 1.2GHz Quad Arm Cortex-A53 (up to 1.5GHz) and 600MHz Dual Cortex-R5 Cores

4GB DDR4 SDRAM (64bit, 2400MHz)

4GB eMMC Flash, 128MB QSPI Flash

L1 Cache 32KB I / D per core, L2 Cache 1MB, on-chip Memory 256KB

Mali™-400 MP2 up to 667 (on ZU4EV and ZU5EV FPGA)

Video Codec : H264/H265 (on ZU4EV and ZU5EV FPGA)

Dynamic Memory Interface : 32/x64: DDR4, LPDDR4, DDR3, DDR3L, LPDDR3 with ECC

Static Memory Interfaces : NAND, 2x Quad-SPI

I/O : PCIe Gen2 x4

2x USB

2x SC/SDIO

2x UART

2x CANbus

1x Gigabit Ethernet

1x SATA

4x 32b GPIO

2x I2C

2x SPI

1x WatchDog

Video 1x Display port

System Monitor 10-bit, 1MSPS – Temperature and Voltage

Operating Temperature 0° to 70° or -40 ° to 85°

### RELATED PRODUCT





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## Zynq Ultrascale + FPGA Module

### ► OVERVIEW

The NES-MFZUC+ FPGA Module is a powerful System-on-Module (SoM) based on Xilinx Zynq UltraScale+ ZU3EG / ZU4EV/ZU5EV which features a 1.2 GHz quad-core ARM Cortex-A53 64-bit application processor, a 600MHz dual-core real-time ARM Cortex-R5 processor, a Mali400 MP2 embedded GPU and rich FPGA fabric. It has 4GB DDR4, 4GB eMMC and 128MB QSPI Flash default memory configuration on board as well as integrated Ethernet PHY, USB PHY and Power Module to provide control and processing capabilities as a minimum embedded system. It offers easy access to signals carried to or from the MPSoC through two 0.5mm pitch 160-pin Razor Beam High-Speed Sockets. It is ready to run PetaLinux and support Xilinx Vitis Software development platform, which comes with detailed documentations and software package.

#### Order Part Number:

- NES-MFZUC+-ZU3EG FPGA Module with ZU3EG
- NES-MFZUC+-ZU4EV FPGA Module with ZU4EV
- NES-MFZUC+-ZU5EV FPGA Module with ZU5EV